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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**  
( Not for submission under 37 CFR 1.99)

Application Number	09989762
Filing Date	2001-11-19
First Named Inventor	Kenneth Y. Ogami
Art Unit	2123
Examiner Name	Osborne, Luke R.
Attorney Docket Number	CD01175M

**U.S.PATENTS**

Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	5155836	B1	1992-10-13	Jordan et al.	Entire Document
	2	5574852	B1	1996-11-12	Bakker, et al.	Entire Document
	3	5495593	B1	1996-02-27	Elmer, et al.	Entire Document
	4	5371878	B1	1994-12-06	Coker	Entire Document
	5	5313618	B1	1994-05-17	Pawloski	Entire Document
	6	4969087	B1	1990-11-06	Tanagawa, et al.	Entire Document
	7	4939637	B1	1990-07-03	Pawloski	Entire Document
	8	4670838	B1	1987-06-02	Kawata	Entire Document

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9	5329471	B1	1994-07-12	Swoboda, et al.	Entire Document
10	7188063	B1	2007-03-06	Snyder, Warren	Entire Document
11	5479643	B1	1995-12-26	Bhaskar et al.	Entire Document
12	6601236	B1	2003-07-29	Curtis	Entire Document
13	5050168	B1	1991-09-17	Paterson	Entire Document
14	6664978	B1	2003-12-16	Kekic et al.	Entire Document
15	6865429	B1	2005-03-08	Schneider et al.	Entire Document
16	6823497	B1	2004-11-23	Schubert et al.	Entire Document
17	6611276	B1	2003-08-26	Muratori et al.	Entire Document
18	6718533	B1	2004-04-06	Schneider et al.	Entire Document
19	5968135	B1	1999-10-19	Teramoto, et al.	Entire Document

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20	6681280	B1	2004-01-20	Miyake, et al.	Entire Document
21	6314530	B1	2001-11-06	Mann	Entire Document
22	5438672	B1	1995-08-01	Dey	Entire Document
23	6202044	B1	2001-03-13	Tzori	Entire Document
24	6356862	B1	2002-03-12	Bailey	Entire Document
25	5903718	B1	1999-05-11	Marik	Entire Document
26	6188975	B1	2001-02-13	Gay	Entire Document
27	6263302	B1	2001-07-17	Hellestrand, et al.	Entire Document
28	6332201	B1	2001-12-18	Chin, et al.	Entire Document
29	6598178	B1	2003-07-22	Yee, et al.	Entire Document

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**U.S.PATENT APPLICATION PUBLICATIONS**

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**FOREIGN PATENT DOCUMENTS**

Examiner Initial*	Cite No	Foreign Document Number <sup>3</sup>	Country Code <sup>2</sup>	Kind Code <sup>4</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	T <sup>5</sup>
	1	06021732	JP	A1	1994-01-28	Kadowaki, Tadao	Entire Document	<input type="checkbox"/>
	2	368398	EP	A1	1990-05-01	Thommen	Entire Document	<input type="checkbox"/>
	3	405055842	JP	A1	1993-03-26	Kondo	Entire Document	<input type="checkbox"/>
	4	404095408	JP	A1	2002-03-27	Nishimaki	Entire Document	<input type="checkbox"/>
	5	0639816A2	EP	A1	1995-02-22	McCollum	Entire Document	<input type="checkbox"/>
	6	404083405	JP	A1	1992-03-17	Yasue	Entire Document	<input type="checkbox"/>
	7	1205848	EP	A1	2002-05-15	Ericsson	Entire Document	<input type="checkbox"/>

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8	1191423A2	EP	A1	2003-02-01	Garnell I Andreu	Entire Document	<input type="checkbox"/>
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**NON-PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>5</sup>
	1	Application Number 09/922,419 (CD00198): "A Power Supply Pump Circuit for a Microcontroller;" Kutz; 38 pages	<input type="checkbox"/>
	2	Application Number 09/912,768 (CD00200): "A Microcontroller having a Dual Mode Relax Oscillator that is Trimmable;" Shutt; 33 pages	<input type="checkbox"/>
	3	Application Number 09/893,048 (CD00231): "A Microcontroller having an On-Chip High Gain Amplifier;" Kutz; 22 pages	<input type="checkbox"/>
	4	Application Number 09/826,397 (CD00190): "Method and Circuit for Allowing a Microprocessor to Change its Operating Frequency on-the-fly;" Sullam; 24 pages	<input type="checkbox"/>
	5	Application Number 09/887,955 (CD00197): "Novel Power on Reset Circuit for Microcontroller;" Kutz; 42 pages	<input type="checkbox"/>
	6	Application Number 10/001,478 (CD01213M): "In-Circuit Emulator and POD Synchronized Boot;" Nemecek; 44 pages	<input type="checkbox"/>
	7	Application Number 10/001,568 (CD01214M): "Combined In-Circuit Emulator and Programmer;" Nemecek; 47 pages	<input type="checkbox"/>
	8	Application Number 10/002,217 (CD01207M): "Conditional Branching in an In-Circuit Emulation System;" Nemecek; 43 pages	<input type="checkbox"/>

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9	Application Number 10/004,039 (CD01222M): "In-Circuit Emulator with Gatekeeper for Watchdog Timer;" Nemecek; 46 pages	<input type="checkbox"/>
10	Application Number 10/004,197 (CD01220M): "In-Circuit Emulator with Gatekeeper Based Halt Control;" Nemecek; 47 pages	<input type="checkbox"/>
11	From Application Number 10/033,027: CD00187; "A Configurable Input/Output Interface for a Microcontroller"; 09/14/01; 09/953,423; Kutz et al.; 28 pages	<input type="checkbox"/>
12	Application Number 10/001,477 (CD01203M): "Breakpoint Control in an In-Circuit Emulation System;" Roe; 43 pages	<input type="checkbox"/>
13	Application Number 10/000,383 (CD00189): "System and Method of Providing a Programmable Clock Architecture for an Advanced Microcontroller;" Sullam; 34 pages	<input type="checkbox"/>
14	Application Number 09/887,923 (CD00229): "Novel Method and System for Interacting between a Processor and a Power on Reset to Dynamically Control Power States in a Microcontroller;" Kutz; 44 pages	<input type="checkbox"/>
15	Application Number 09/923,461 (CD00192): "Non-Interfering Multiply-Mac (Multiply Accumulate) Circuit;" Snyder; 25 pages	<input type="checkbox"/>
16	Application Number 09/922,579 (CD00230): "A Method for a Efficient Supply to a Microcontroller;" Kutz; 37 pages	<input type="checkbox"/>
17	Application Number 10/011,214 (CD00191): "Method and Circuit for Synchronizing a Write Operation between an On-Chip Microprocessor and an On-Chip Programmable Analog Device Operating at Different Frequencies;" Sullam; 49 pages	<input type="checkbox"/>
18	Application Number 09/935,454 (CD00194): "Method and Apparatus for Local and Global Power Management in a Programmable Analog Circuit;" Mar; 51 pages	<input type="checkbox"/>
19	HINTZ ET AL.; "MICROCONTROLLERS", 1992, McGraw-Hill; 11 pages	<input type="checkbox"/>

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20	GANAPATHY, GOPI, AND NARAYAN, RAM, AND JORDEN, GLEN, AND FERNANDEZ, DENZIL, AND WANG, MING, AND NISHIMURA, JIM; "Hardware Emulation for Functional Verification of K5", June 1996, 33rd Design Automation Conference Proceedings, 3-7 June 1996; 4 pages	<input type="checkbox"/>
21	The Provisional Application 60/243,708 (CD00167P) "Advanced Programmable Microcontroller Device"; 277 pages	<input type="checkbox"/>
22	"Webster's Third New International Dictionary", 1996, G. & C. Merriam Company; 3 pages	<input type="checkbox"/>
23	USPTO Advisory Action for 09/998,848 (CD01177) dated 09/07/05; 3 pages	<input type="checkbox"/>
24	Application Number 09/957,084 (CD00201): "A Crystal-Less Oscillator with Trimmable Analog Current Control for Increased Stability;" Mar; 28 pages	<input type="checkbox"/>
25	Application Number 09/855,868 (CD00181): "Protecting Access to Microcontroller Memory Blocks;" Snyder; 28 pages	<input type="checkbox"/>
26	Application Number 09/972,319 (CD00179): "Method for Applying Instructions to Microprocessor in Test Mode;" Snyder; 31 pages	<input type="checkbox"/>
27	Application Number 09/972,003 (CD00176): "Test Architecture for Microcontroller Providing for a Serial Communication Interface;" Snyder; 32 pages	<input type="checkbox"/>
28	Application Number 09/972,133 (CD00178): "Method for Entering Circuit Test Mode;" Snyder; 30 pages	<input type="checkbox"/>
29	Application Number 09/973,535 (CD00193): "Architecture for Decimation Algorithm;" Snyder; 26 pages	<input type="checkbox"/>
30	Application Number 09/977,111 (CD00202): "A Frequency Doubler Circuit with Trimmable Current Control; Shutt; 35 pages	<input type="checkbox"/>

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31	Application Number 10/272,231 (CD00170C): "Digital Configurable Macro Architecture;" Snyder; 36 pages	<input type="checkbox"/>
32	Application Number 11/125,554 (CD00230C): "A Method for a Efficient Supply to a Microcontroller;" Kutz; 1 page	<input type="checkbox"/>

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